

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki, et al. Art Unit : 1765
Serial No. : 09/304,523 Examiner : Robert Kunemund
Filed : May 4, 1999 Confirmation No.: 9801
Title : METHOD AND APPARATUS FOR PRODUCING SEMICONDUCTOR
DEVICE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


INFORMATION DISCLOSURE STATEMENT

Copies of the references listed on the attached form PTO-1449 are enclosed.

This filing is being made with the filing of a Request for Continued Examination. No fee is required.

Respectfully submitted,

Date: September 16, 2003



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Substitute Form PTO-1449
(Modified)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
07977-046002Application No.
09/304,523**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant
Shunpei Yamazaki, et al.Filing Date
May 4, 1999Group Art Unit
1765**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,585,291	12/17/1996	Ohtani et al.			
	AB	5,869,362	02/09/1999	Ohtani			
	AC	5,837,619	11/17/1998	Adachi et al.			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL	07-146470	06/06/1995	JAPAN			ABS	
	AM	07-161635	06/23/1995	JAPAN			ABS	
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	
	AR	
	AS	
	AT	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.